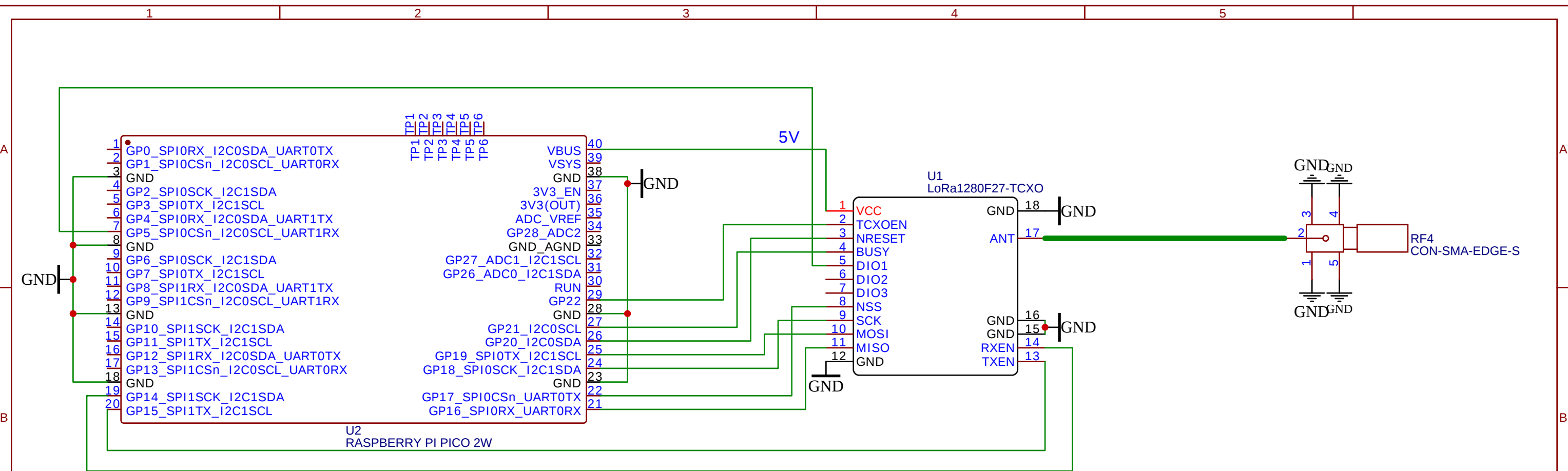




Raspberry Pi Pico 2		LoRa1280F27-TCXO Module
GPIO 16 (SPI0 RX)	-----	MISO
GPIO 17	-----	NSS (CS)
GPIO 18 (SPI0 SCK)	-----	SCK
GPIO 19 (SPI0 TX)	-----	MOSI
GPIO 20	-----	RESET
GPIO 21	-----	BUSY
GPIO 22	-----	TCXO_EN
GPIO 14	-----	RX_EN
GPIO 15	-----	TX_EN
VBUS (5V)	-----	VCC
GND	-----	GND
USB	-----	To computer (Audio + CDC)

LoRa1280F27		Raspberry Pi Pico
1	VCC	VBUS (Pin 40)
2	GND	GND (Pin 38)
3	NRESET	GPIO20 (Pin 26)
4	BUSY	GPIO21 (Pin 27)
5	DIO1	dio1 podłączony pod gpio5
6	DIO2	(niepodłączony)
7	DIO3	(niepodłączony)
8	NSS	GPIO17 (Pin 22)
9	SCK	GPIO18 (Pin 24)
10	MOSI	GPIO19 (Pin 25)
11	MISO	GPIO16 (Pin 21)
12	GND	GND
13	TXEN	GPIO15 (Pin 20)
14	RXEN	GPIO14 (Pin 19)
15	GND	GND
16	GND	GND
17	ANT	Antena 2.4GHz
18	GND	GND

https://github.com/SP8ESA/SX1280_QO100_SSB_TX/releases/tag/v1.0.0
[v.1.4.1 https://github.com/SP8ESA/SX1280_QO100_SSB_TX](https://github.com/SP8ESA/SX1280_QO100_SSB_TX/releases/tag/v1.4.1)
https://github.com/SP8ESA/SX1280_QO100_SSB_TX/releases/tag/v1.4.1
https://github.com/SP8ESA/SX1280_QO100_SSB_TX/releases/tag/v1.5.0

SX1280 QO-100 SSB TX

TITLE: SX1280 QO-100 SSB TX		REV: 1.0
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	Date: 2025-01-18	Drawn By: sp8ntp